IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jae-goo Lee

Serial No.: To Be Assigned Filed: Concurrently Herewith

For: INTEGRATED CIRCUIT MEMORY DEVICES

January 13, 2004

PATENT

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a list of documents on form PTO-1449 which were cited in parent application Serial No. 10/100,719, filed March 19, 2002. As the benefit of this application is claimed under 35 U.S.C. §120, no copies need to be furnished in accordance with 37 C.F.R. §1.98(d); however, copies will be furnished on request. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.56 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,

David K. Purks

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APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Audra Wooten

FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office					Attorney Docket Number: 5649-951DV			Serial No.:
LIST OF DOCUMENTS CITED BY APPLICANT								To Be Assigned
(Use several sheets if necessary)					Applicants: Jae-goo Lee			
·					Applicants. Jac-goo Lee			
					Filing Date: Concurrently Herewith			Group: Unknown
U. S. PATENTS & PATENT APPLICATION PUBLICATIONS								
Examiner Initial		Document Number	Date		Name	Class	Subclass	Filing Date if Appropriate
	1	5,744,833	04-28-98	Chae		257	308	
	2	5,879,982	03-09-99	Park et al.		438	241	
	3	6,037,215	03-14-00	Lee et al.		438	253	
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country		Class	Subclass	Translation Yes No
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
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